

# **EVLA Correlator Project Management Review**

*Amy Fink*



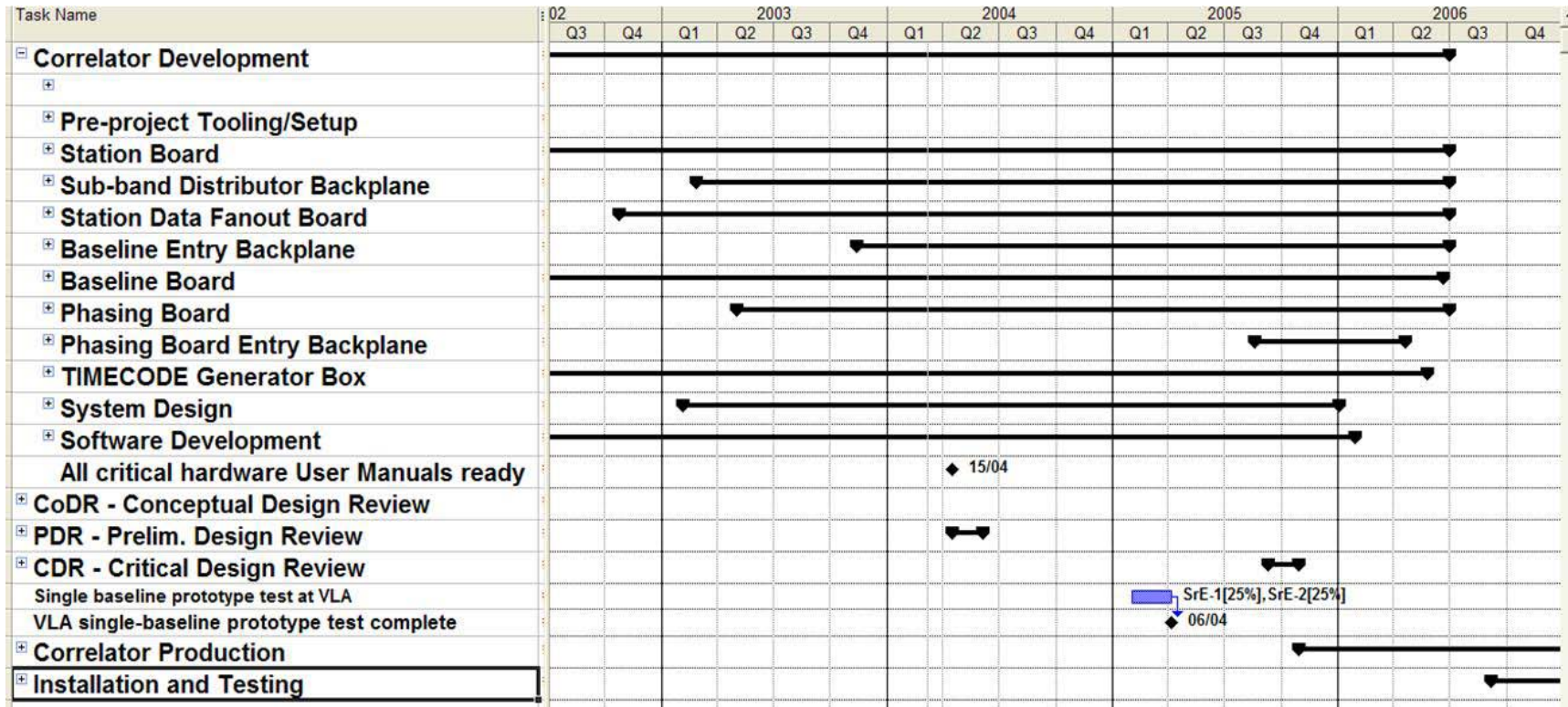
# **EVLA Correlator Project Management**

- Original Schedule and Budget
- Interthink Consulting Proposals
- Work done in the Last Year
- Set-Backs/Slow-Downs in Last Year
- Project Management Status Now
- Project Management in the Future

## Original Schedule & Budget

- Project Schedule as of 28Dec2001 (developed by B. Carlson)
- Project Budget as of 18Jan2002 (developed by B. Carlson)

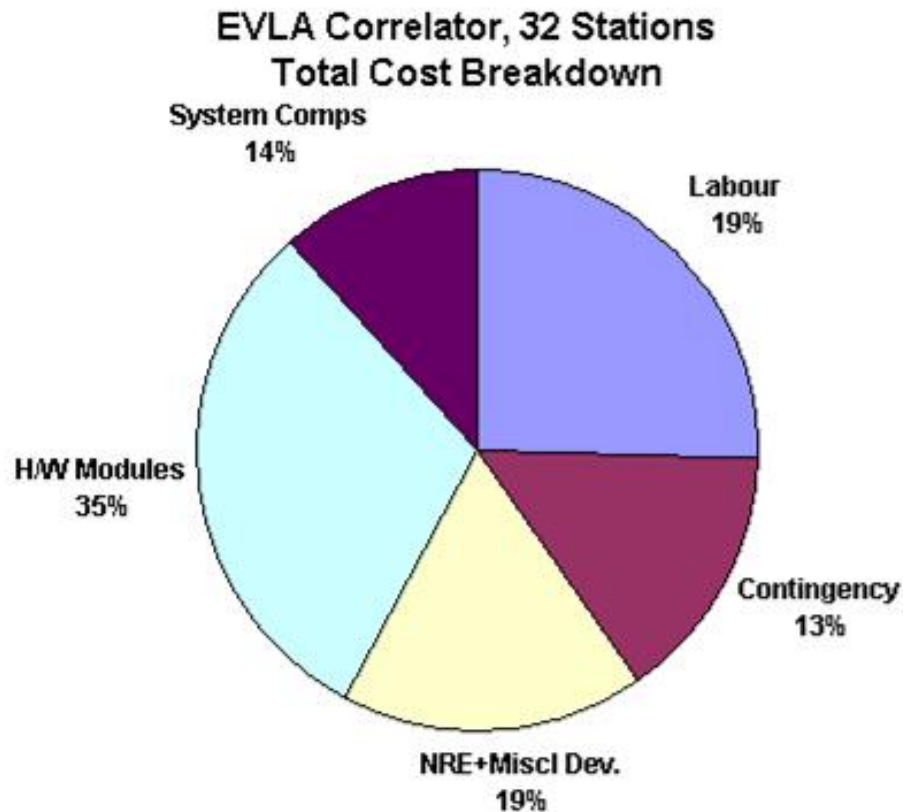
# Project Schedule as of 28Dec2001 – WBS Outline



# **Project Schedule as of 28Dec2001 – Milestone Dates**

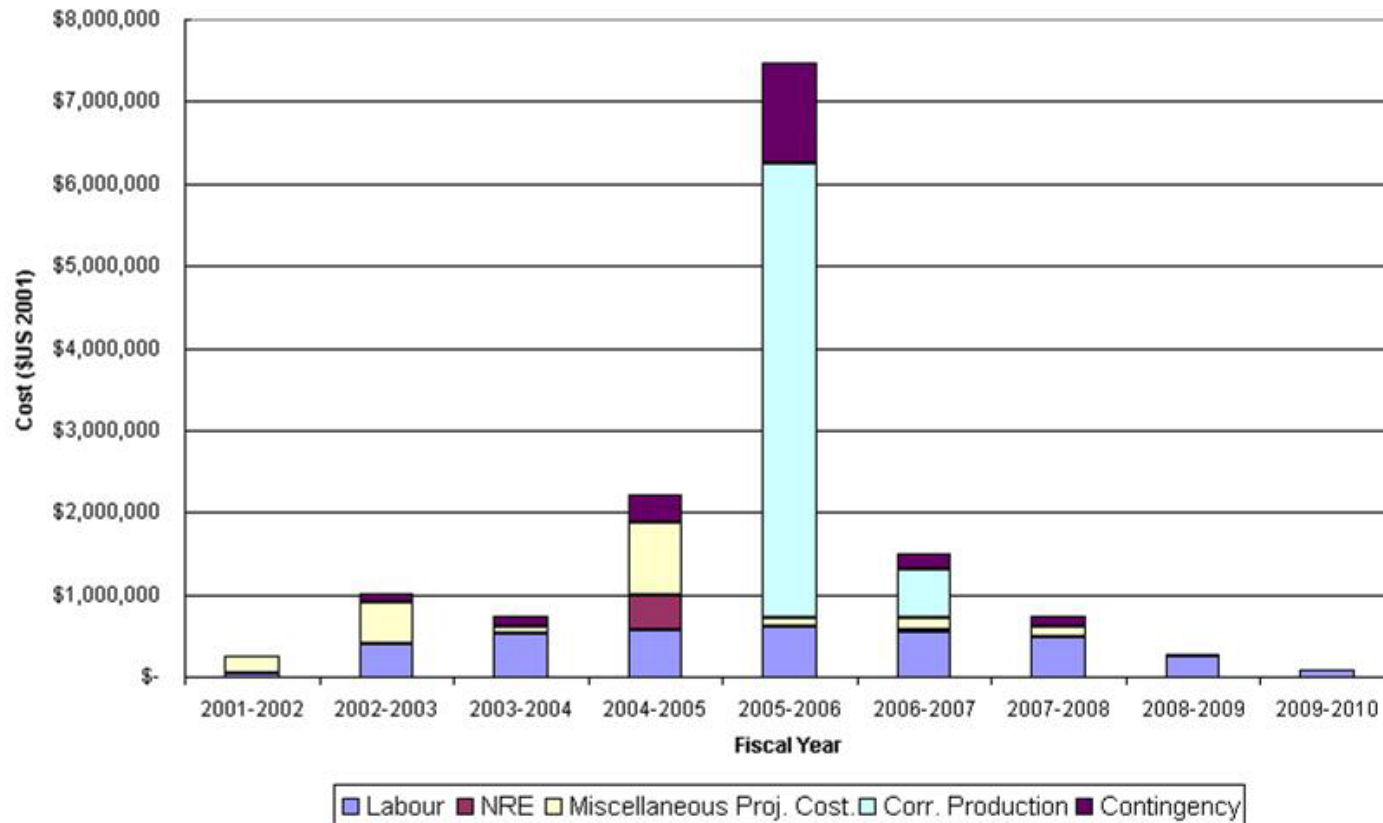
- Critical Hardware Manuals: Apr2004
- PDR: May2004
- FIR Filter Prototype: Jan2005 – Jun2005
- Correlator Chip Prototype: Jan2005 – Jun2005
- VLA Single Baseline Prototype Test: Apr2005
- CDR: Oct2005
- FIR Filter Production: Oct2005 – Feb2006
- Correlator Chip Production: Oct 2005 – Feb2006
- System Integration & Test (Penticton): Jul2006 – Feb2007
- System Installation & Test (VLA): Feb2007 – Apr2008
- Commissioning: Apr2008

# Project Budget as of 18Jan2002 – Cost Breakdown



# Project Budget as of 18Jan2002 – By Fiscal Year (2001 USD)

EVLA Correlator Fiscal Year Budget  
(\$US 2001)

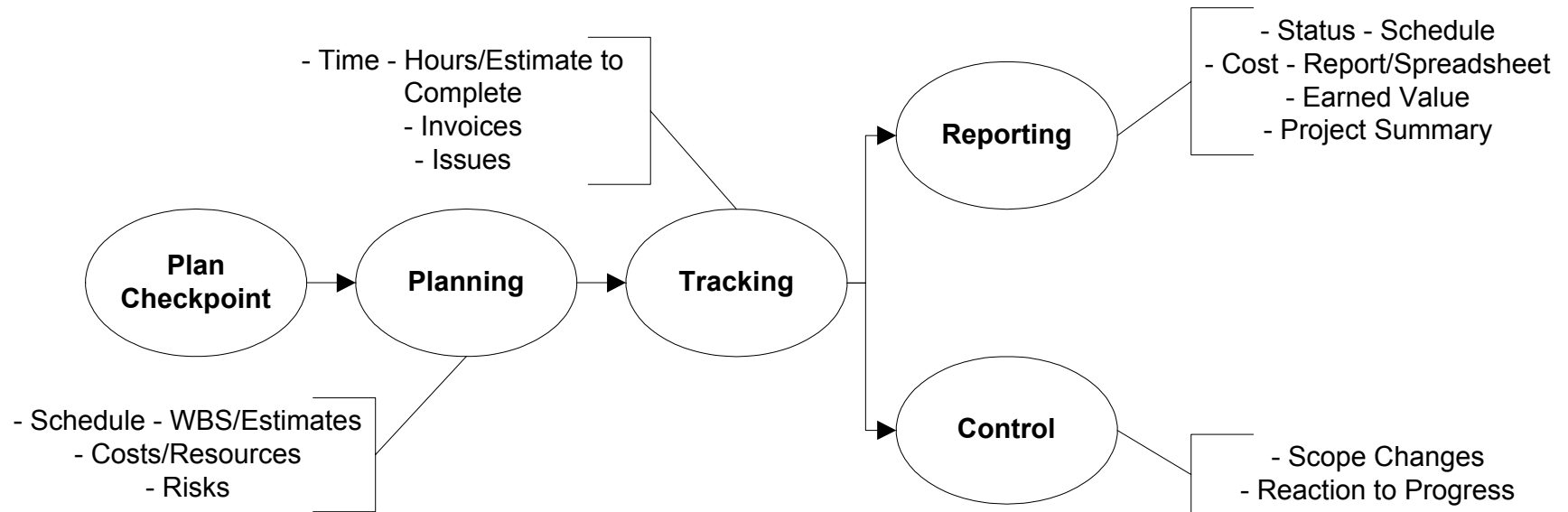


# Interthink Consulting Proposals

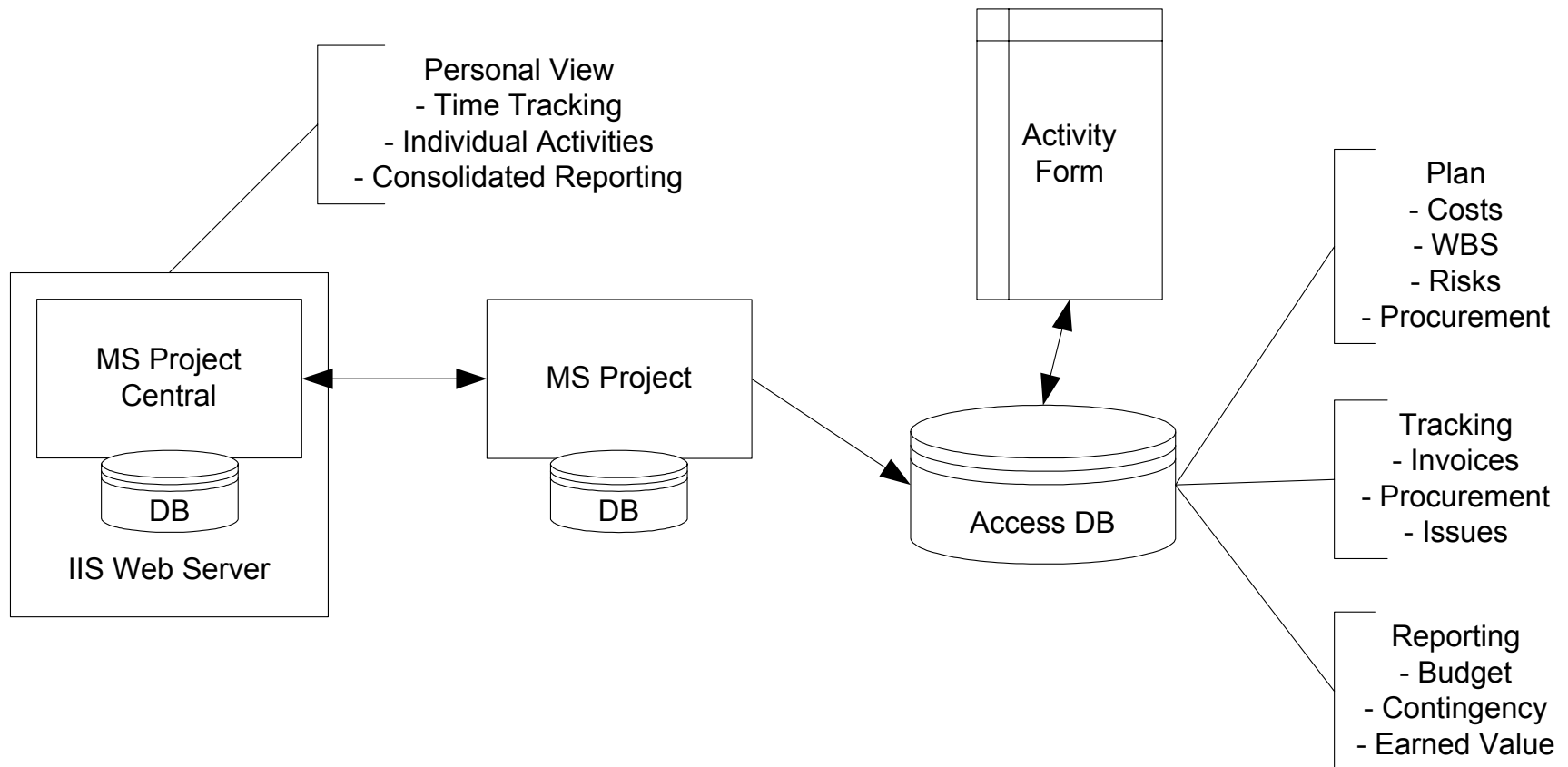
- Restructure the WBS
- Time Based Schedule
- Revamp Resource & Time Estimates
- MS Access Budget Tracking Database



# Interthink Consulting Proposals



# Interthink Consulting Proposals



## **Work Done in the Last Year**

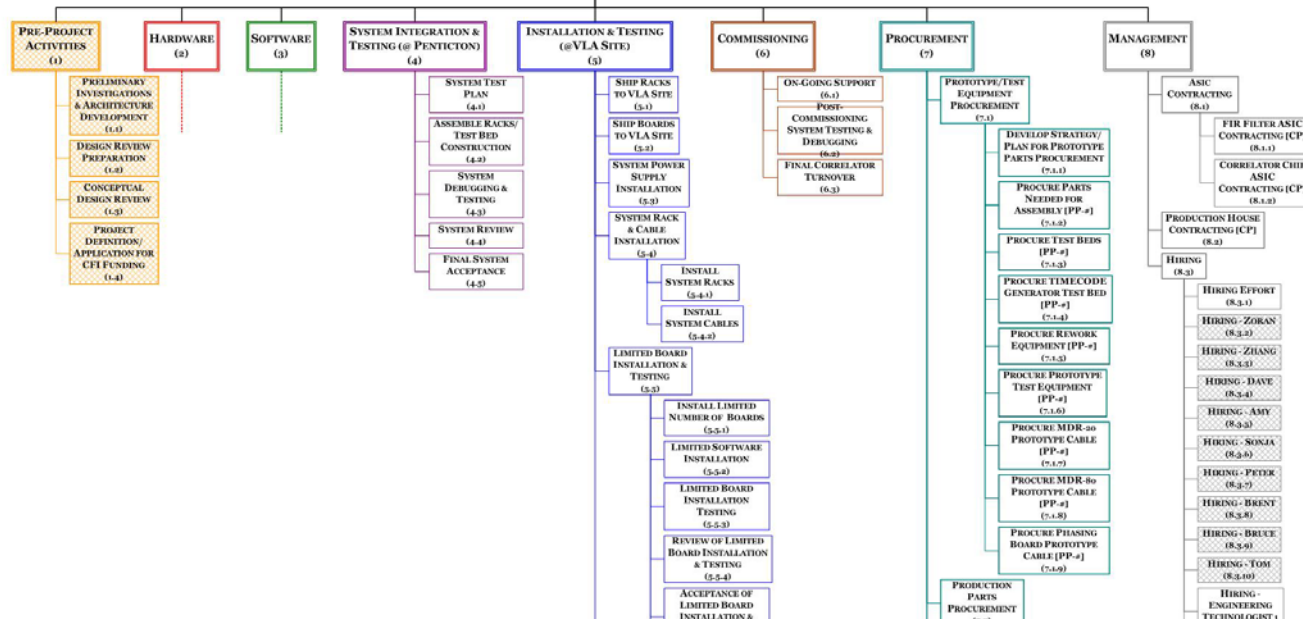
- Project Management Meetings
- Created Activity Form
- WBS Dictionary Created
- MS Access Database Created
- DRAO-EVLA Web Site Remodeled
- Project Reporting Scheme Developing

## **Work Done in the Last Year**

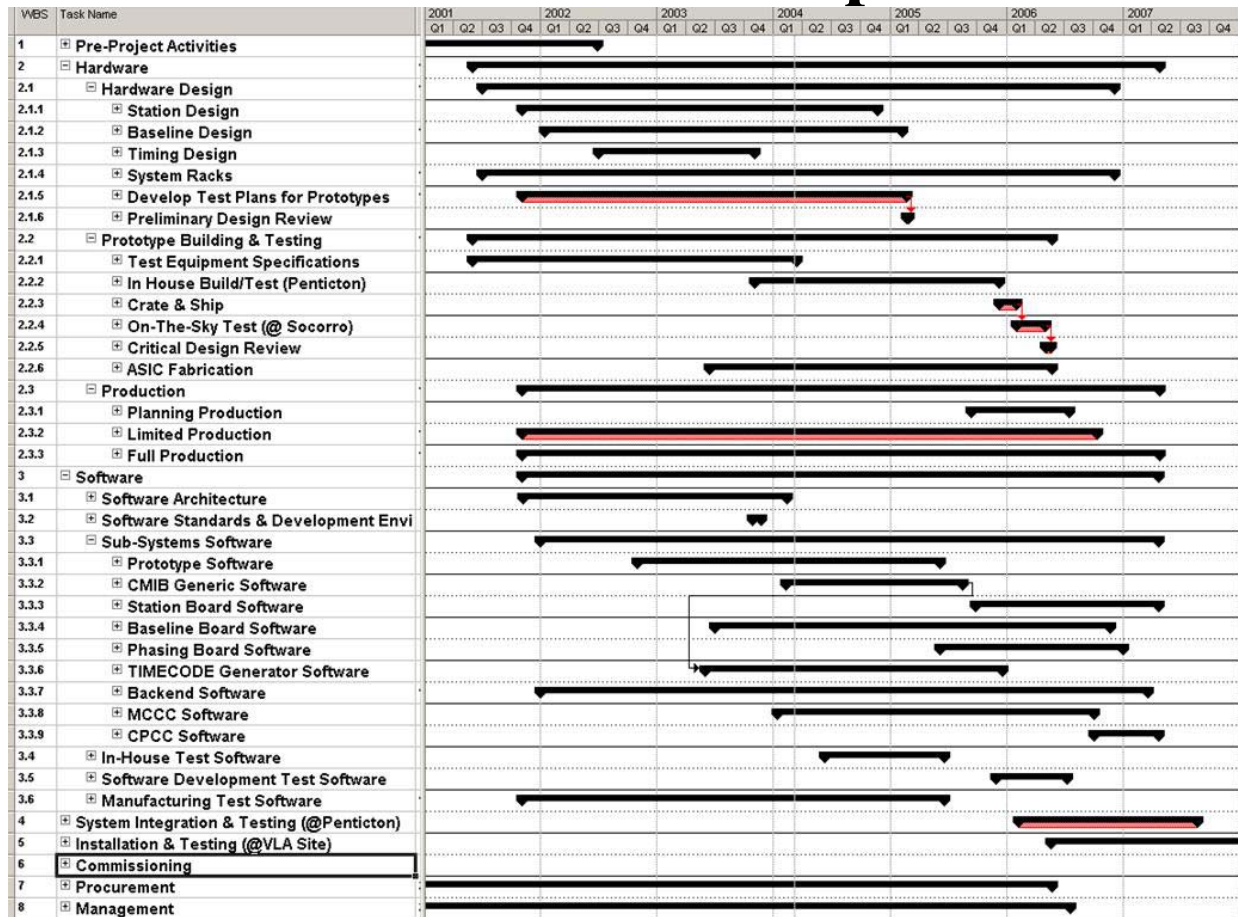
- Project Management Meetings
  - WBS Re-organized & Re-formatted
  - Schedule Revamped
  - Cycles of Work Created

# Work Done in the Last Year – WBS Re-organized & Re-formatted

## EVLA PROJECT WBS



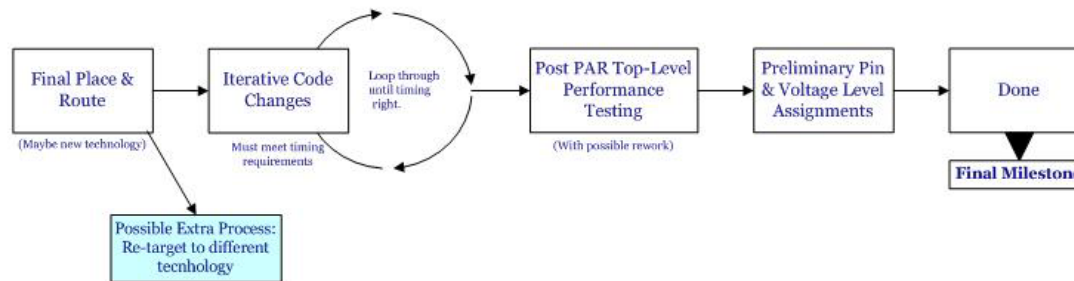
# Work Done in the Last Year – Schedule Revamped



# Work Done in the Last Year - Cycles of Work Created

## FPGA PROCESS (4) - CYCLE OF WORK

### FDP-4: Final Place & Route



## Work Done in the Last Year

- Created Activity Form
  - Time Estimates Gathered
  - Resource Allocations Identified
  - Task Definitions Gathered
  - Project Risks Identified (Not Complete)



# Work Done in the Last Year – Created Activity Form

**NRC · CNRC**

**EVLA Project**

Work Breakdown Structure Dictionary

ID:	_____	WBS Code:	_____
Name:	_____	New (Useful) Name:	_____

Description:

Assumptions:

Deliverables:

Risk Identification:

Linkages:

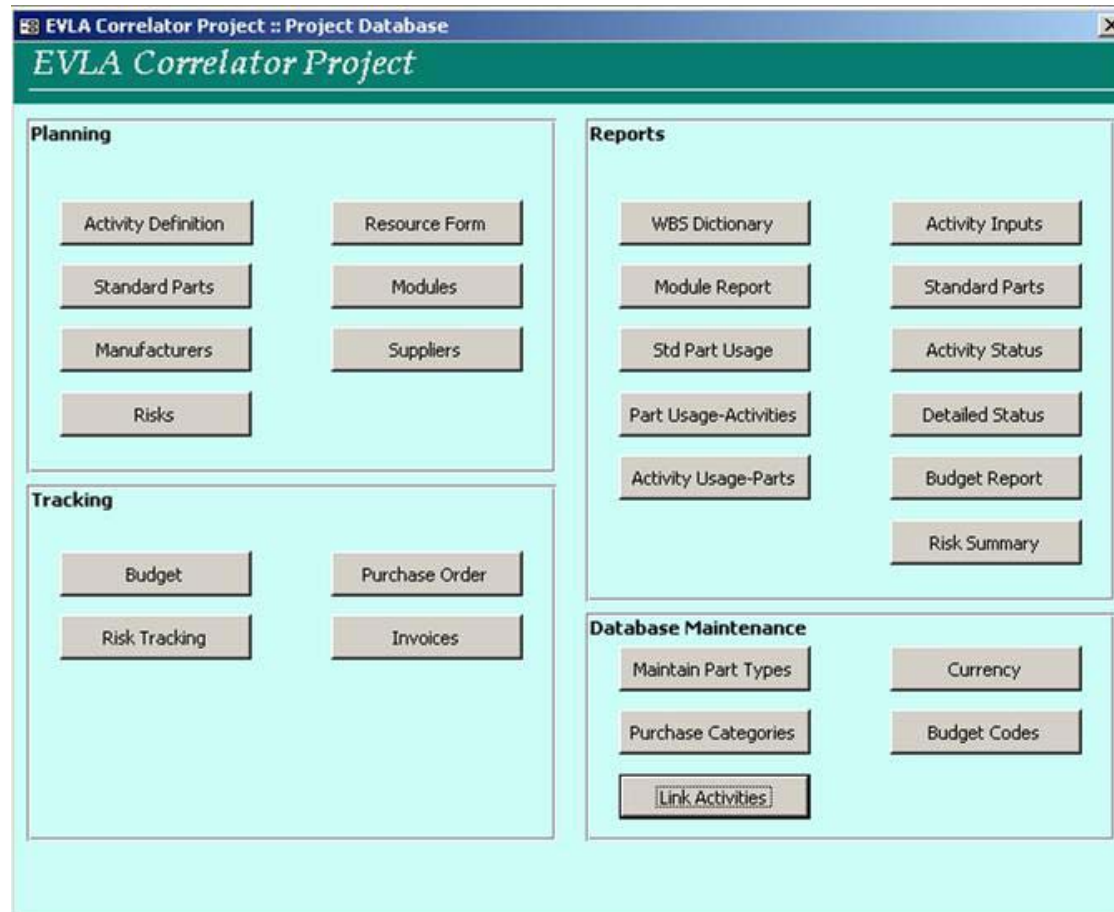
Staff Resource Identification	Estimate to Complete (in Hours)		
	Best Case	Worst Case	Most Likely

# Work Done in the Last Year – WBS Dictionary Created

## *EVLA Project WBS Dictionary*

<b>WBS Code:</b> 1	<b>Name:</b> Pre-Project Activities
<b>Description:</b>	Higher-Level Task: Placeholder for lower level activities.
<b>WBS Code:</b> 1.1	<b>Name:</b> Preliminary Investigations & Architecture Development
<b>WBS Code:</b> 1.2	<b>Name:</b> Design Review Preparation
<b>WBS Code:</b> 1.3	<b>Name:</b> Conceptual Design Review
<b>WBS Code:</b> 1.4	<b>Name:</b> Project Definition/Application For CFI Funding
<b>WBS Code:</b> 2	<b>Name:</b> Hardware
<b>Description:</b>	Higher-Level Task: Placeholder for lower level activities.
<b>WBS Code:</b> 2.1	<b>Name:</b> Hardware Design
<b>Description:</b>	Higher-Level Task: Placeholder for lower level activities.
<b>WBS Code:</b> 2.1.1	<b>Name:</b> Station Design
<b>Description:</b>	Higher-Level Task: Placeholder for lower level activities.
<b>WBS Code:</b> 2.1.1.1	<b>Name:</b> Station Board
<b>Description:</b>	Higher-Level Task: Placeholder for lower level activities.
<b>WBS Code:</b> 2.1.1.1.1	<b>Name:</b> Station Board Architecture Review
<b>WBS Code:</b> 2.1.1.1.2	<b>Name:</b> Delay Module Preliminary Design
<b>WBS Code:</b> 2.1.1.1.3	<b>Name:</b> Station Board Fibre Optic Receiver Module ICD & User Manual (from NRAO)
<b>Description:</b>	<ul style="list-style-type: none"> <li>Get symbol, footprint, documents for this NRAO designed mezzanine card.</li> </ul>
<b>Assumptions:</b>	<ul style="list-style-type: none"> <li>Card works like we think it does – very little documentation at this time.</li> </ul>
<b>Deliverables:</b>	<ul style="list-style-type: none"> <li>Mechanical and electrical specifications.</li> <li>Interface Control Document/User Manual (from NRAO)</li> </ul>
<b>Risks:</b>	<ul style="list-style-type: none"> <li><b>Risk:</b> Signal integrity across the connectors (5% probability of occurrence). <b>Impact:</b> Will require redesign of interface.</li> <li><b>Risk:</b> Late delivery of documents (5% probability of occurrence). <b>Impact:</b> Delays Station Board PCB Design.</li> </ul>
<b>Linkage:</b>	<ul style="list-style-type: none"> <li>NRAO must complete design and ICD/User Manual required before Station Board PCB design and layout can proceed.</li> </ul>
<b>Resource:</b>	<ul style="list-style-type: none"> <li>Dave F. (60h)</li> </ul>

# Work Done in the Last Year – MS Access Database Created



# Work Done in the Last Year – DRAO-EVLA Web Site Remodeled

**NRC - CNRC**

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PRIVATE DOCUMENTS


- MEMOS
- SYSTEM
- STATION BOARD
- BASLINE BOARD
- PHASING BOARD
- OTHER BOARDS
- APPLICATION NOTES
- SOFTWARE
- CODING STANDARDS
- MEETINGS
- TEMPLATES
- MANAGEMENT

CONTRACT DOCUMENTS

WIDAR HOME

DRAO HOME

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**EVLA - WIDAR**

EXPANDED VERY LARGE ARRAY      WIDEBAND INTERFEROMETRIC DIGITAL ARCHITECTURE

**BASLINE BOARD DOCUMENTS**

DOCUMENTS RELEVANT TO THE DESIGN OF THE BASELINE BOARD.

BASELINE BOARD DOCUMENT TITLE	DOCUMENT NUMBER	DATE	AUTHOR
EVLA CORRELATOR CHIP [RFS]	A25082N0000	25NOV2003	BRENT CARLSON
EVLA CORRELATOR CHIP TESTING SOFTWARE [RFS]	A25082N0010	27MAY2003	FRANCES LAU
EVLA CORRELATOR CHIP TEST ANALYSIS SOFTWARE [RFS]	A25082N0011	16JUL2003	FRANCES LAU
RECIRCULATION CONTROLLER FPGA [RFS]	A25090N0000	27AUG2003	BRENT CARLSON
LTA CONTROLLER FPGA [RFS]	A25091N0000	23FEB2004	BRENT CARLSON

[BACK TO WIDAR HOME PAGE](#)

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AMY FINK  
LAST MODIFIED ON 01MAR2004

## **Work Done in the Last Year – Project Reporting Scheme Developing**

- Reporting Checklist/Plan Created
- Sample Reports Created
- Request Forms Created

# Work Done in the Last Year – Project Reporting Scheme Developing

<b>EVLA Reporting Checklist</b>												
<b>Date Produced:</b>		09Feb2004										
<b>Quarter to Report:</b>		Q1 2004										
<b>Weekly Reports:</b>												
<b>Report Name</b>	<b>#1</b>	<b>#2</b>	<b>#3</b>	<b>#4</b>	<b>#5</b>	<b>#6</b>	<b>#7</b>	<b>#8</b>	<b>#9</b>	<b>#10</b>	<b>#11</b>	<b>#12</b>
Staff Status Report – B. Carlson	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – A. Fink	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – D. Fort	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – Z. Heng	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – Z. Ljusic	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – T. Morgan	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – B. Rowen	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Staff Status Report – S. Vrcic	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Consultant Status Report – Aardvark	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Consultant Status Report – HDD	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Consultant Status Report – Interthink	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
<b>Monthly Reports:</b>												
<b>Report Name</b>	<b>#1</b>	<b>#2</b>	<b>#3</b>									
Operating Budget Report	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>									
Procurement Report	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>									
Web Update Report	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>									
CAD/USD Conversion Rate Graph	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>									
<b>Quarterly Reports:</b>												
<b>Report Name</b>	<b>Date</b>	<b>Complete</b>										
Quarterly Status Report		<input type="checkbox"/>										
Task Percent Complete Report		<input type="checkbox"/>										
Project WBS Dictionary		<input type="checkbox"/>										
Project Management Report		<input type="checkbox"/>										
Spending/Projected Spending Report		<input type="checkbox"/>										
WBS Map Updates		<input type="checkbox"/>										
<b>Notes:</b>												
<ul style="list-style-type: none"> <li>The list of reports and required actions on this checklist may not yet be complete.</li> </ul>												

# Work Done in the Last Year – Project Reporting Scheme Developing

## EVLA Project Management Report

Report Date: 09Feb2004  
Reporting Period: January 2004

### Schedule Updates:

**Changes:**

- No changes were made to the schedule in the last reporting period

**Comments:**

- Problem still exists where software is delaying the overall project schedule by many months

### WBS Updates:

**New Tasks:**

- No new tasks were created in the last reporting period.

**Deleted Tasks:**

- No tasks deleted in the last reporting period.

**Comments:**

- Procurement tasks still need to be created to accurately forecast project spending
- Still need to split the FPGA development process into smaller tasks to eliminate the scheduling conflict resulting from pinout definitions not being completed until later dates

### WBS Dictionary Updates:

**Changes:**

- No changes were made to the WBS dictionary in the last reporting period

**Comments:**

- Still need to finish defining all tasks in the present dictionary
- Dictionary will have to be reconciled with the WBS when the aforementioned changes are made to it.

### WBS Map Updates:

**Changes:**

- No changes were made to the WBS Map(s) in the last reporting period

**Comments:**

- Need to reconcile WBS map with changes made in the last few months to the WBS and to update the task percent complete as indicated by the map.

### Project Risk Updates:

**New Risks:**

- Possibility that the FIR filter chip will not meet timing specs.  
Probability: 50% Impact: 5

**Obsolete Risks:**

- No risks have become obsolete in the last reporting period

**Changes to Current Risk Probability and/or Impact:**

- All risks remain the same in terms of probability and impact.

**Contingency Changes:**

- Contingency remains the same after the last reporting period.

# Work Done in the Last Year – Project Reporting Scheme Developing

## EVLA Procurement Request Form

**Date Submitted:** 29 Jan 2004 **Request No.:** BC-29012004-0  
**Submitted By:** Brent Carlson

### Goods/Services Requested:

Part #	Name	Qty	Est. Unit Cost (USD/CAD)
16702B	Logic Analysis System Frame	1	\$18,635.00 CAD
16702B-003	256 Mbyte System RAM, 4 Mbyte Video RAM	1	\$4,252.00 CAD
16754A	4M 600MHz State/4GHz LA Mod	1	\$43,175.00 CAD
E5387A	17-ch diff. Soft Touch Connectorless Probe	2	\$3,797.00 CAD
E5381A	17-ch diff. Flying Leas	1	\$3,658.00 CAD

### Supplier(s):

**Agilent Technologies** Steve Hall (steve\_hall@agilent.com)  
 Web: [www.agilent.com](http://www.agilent.com)

### Notes:

- You will have to iterate with me to write detailed specs for PWGSC, since there is not a sole-source justification.
- Get pricing from Agilent in USD – the CAD prices above do not reflect the rise of the CAD that well.
- The Agilent guy to contact is Steve Hall. I've attached spec sheets & email.



## **Set-Backs/Slow Downs in the Last Year**

- Learning Curve - Amy
- Overhauling the WBS
- Project Management Consultant
- Project Central

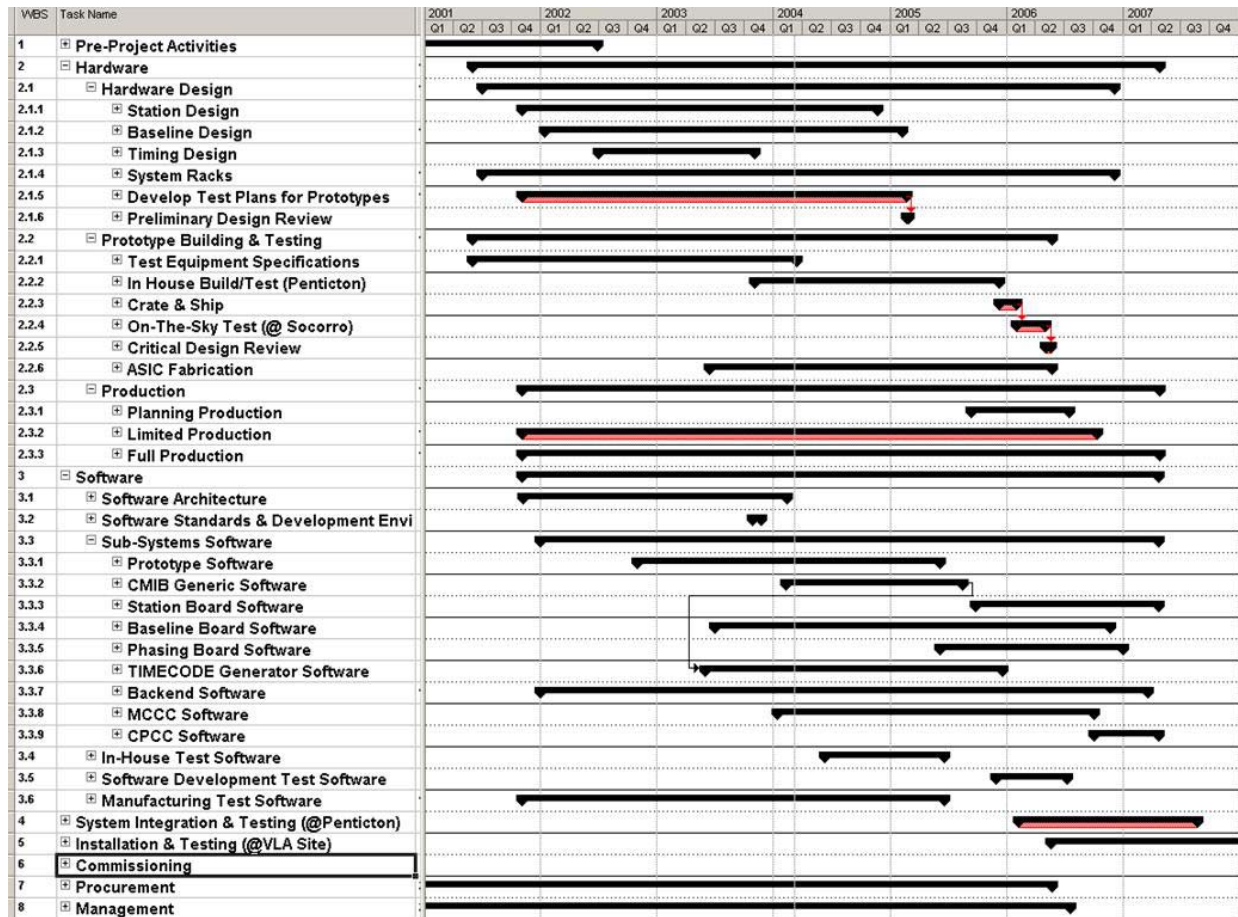
## **Project Management Status Now**

- Current Schedule in Good Working Order but Incomplete
- MS Access Tracking Database Made but Incomplete
- Project Reporting Outline Created but Plan not Finalized

## **Project Management Status Now – Current Schedule**

- Baseline Set beginning Today – 09Mar2004
- Major Structure and Resource & Time Estimates Complete
- Project Central Running
- Procurement Tasks Need Scheduling
- Further Scheduling Needed on some Tasks (Procurement, Engineering Technicians, Some Hardware Specs, etc.)

# Project Management Now – Current Schedule



# **Project Management Status Now – Current Schedule**

- Correlator Chip ASIC Prototype Fabrication: Jun2004 – Dec2004
- PDR: Feb2005 – Mar2005
- FIR Filter ASIC Prototype Fabrication: Sep2005 – Mar2006
- Hardware/Software Integration Testing: Oct2005 – Dec2005
- Final System Design Review (Penticton): Dec2005
- On-The-Sky Testing: Mar2006 – Apr2006
- CDR: May2006
- Limited Production Run: Jul2006 – Sep2006
- Full Production Run: Oct2006 – Jan2007
- Start Shared-Risk Observing: Dec2007
- Commissioning: May2008 – Dec2008

# Project Management Status Now – MS Access Tracking Database

Standard Parts

Standard Parts

Update Part Details

Select a standard part:

Part Type	WIDAR Part No	Part Name:	Description	Est. Price (C\$)
Active (A)		FPGA-MCBIF-25093	10,570 LE Stratix FPGA, 5 speed grade, 780 pin	\$170.00
Active (A)		FPGA-RECIRC-25090	10,570 LE Stratix FPGA, 5 speed grade, 780 pin, Reci	\$170.00
Active (A)		FPGA-Ethernet-25092	10,570 LE Stratix GX FPGA, with 4, Gbit transceivers	\$500.00
Active (A)			12VDC to LVDC converter module (SIP) (NQ series, 2C	\$20.00
Active (A)		DCDC12-1.5	12VDC-to-1.5VDC Converter	\$20.00
Active (A)		DCDC12-2.5	12VDC-to-2.5VDC Converter	\$20.00
Active (A)		DCDC12-3.3	12VDC-to-3.3VDC Converter	\$20.00
Active (A)		DCDC12-5.0	12VDC-to-5.0VDC Converter	\$20.00
Active (A)		DDR-SDRAM	16Mx16, DDR SDRAM, 60-ball FBGA, -75E speed	\$15.05

To identify the module it is used in:

Module	Quantity

To identify the suppliers of the part:

Supplier	Est Price	Lead (d)	Est Date

To identify the activities where it is required:

Activity	Budget	Actual

To identify the manufacturers of the part:

Manufacturer	Mfr Part Number
Altera	EP1S10F780C5
*	

# Project Management Status Now – MS Access Tracking Database

EVLA Operating Budget Report

Budget Code:

Initial Allocation:

Requisition Number	Issue Date	Description	Supplier Name	Purchaser	Travel	Software	Equipment	Misc	Committed
Committed Funds -					#Error	#Error	#Error	#Error	#Error
					Total Committed Funds				#Error
					Free Balance				#Error

# Project Management in the Future

- Project Schedule
- Project Database
- Project Reporting
- Project WBS Dictionary
- Project Risk Identification
- Project Tracking/Management



# **Project Management in the Future – Project Schedule**

- Confirm Resource & Time Estimates
- Plan & Schedule Procurement Activities
- Finish Scheduling Some Tasks
- Continue Project Central Tracking Activities

# **Project Management in the Future – Project Database**

- Add in Remaining Information
  - Past Spending Information
  - Projected Parts/Spending
  - Project Risks
- Link Costs with Tasks
- Create/Forecast Budget
- Begin Tracking Spending

## **Project Management in the Future – Project Reporting**

- Confirm Present Reporting Plan
- Update Reports to Current Format
- Begin Constant Scheduled Reporting
- Set up Reporting Area on Web Site

## **Project Management in the Future – Project WBS Dictionary**

- Update WBS Dictionary to Reflect Current WBS
- Add in Remaining Missing Information
- Maintain and Publish on a Regular Basis

# **Project Management in the Future – Project Risk Identification**

- Finish Identifying all Known Possible Risks
- Associate Risks with Tasks
- Associate Contingencies & Mitigation Plans with Risks

## **Project Management in the Future – Project Tracking/Management**

- Restructure Weekly Meetings
- More Contact with Engineers
- No More Slipping!